

FIG. 3



DDS/PDL

ВР	Contents	Number of bytes
0 to 1	DDS identifier (OAOAh)	2 bytes
2	Reserved	1 byte
3	Disc certification flag	1 byte
4 to 7	//////////////////////////////////////	4 bytes
8 to 9	Number of Groups	2 bytes
10 to 11	Number of zones	2 bytes
12 to 79	Reserved	68 bytes
80 to 87	Location of Primary spare area	8 bytes
88 to 91	Location of LSN 0	4 bytes
92 to 255	Reserved	164 bytes
256 to 259	Start LSN for Zone O	140 bytes
260 to 263	Start LSN for Zone 1	
1 1 1	1	
392 to 395	Start LSN for Zone 34	
396 to 399	DMA rec-counter 1////////////////////////////////////	4 bytes
400 to 2047	reserved	1652 bytes

F1G. 4

SDL

ВР	Contents	Number of bytes
0 to 1	SDL identifier (0002h)	2 bytes
2 to 3	Reserved	2 bytes
4 to 7	SDL update counter	/////4 bytes////////
8 to 11	Start sector number of Supplementary spare area	4 bytes
12 to 15	Total number of logical sectors	4 bytes
16 to 19	DDS/PDL update counter	4 bytes
20	Spare area full flags	1 byte
21 to 24	DMA rec-counter 2////////////////////////////////////	W bytes
25 to 26	Number of entries in SDL	2 bytes
27 to 34	The first SDL entry	8 bytes
		!
M to m+7	The last SDL entry	8 bytes

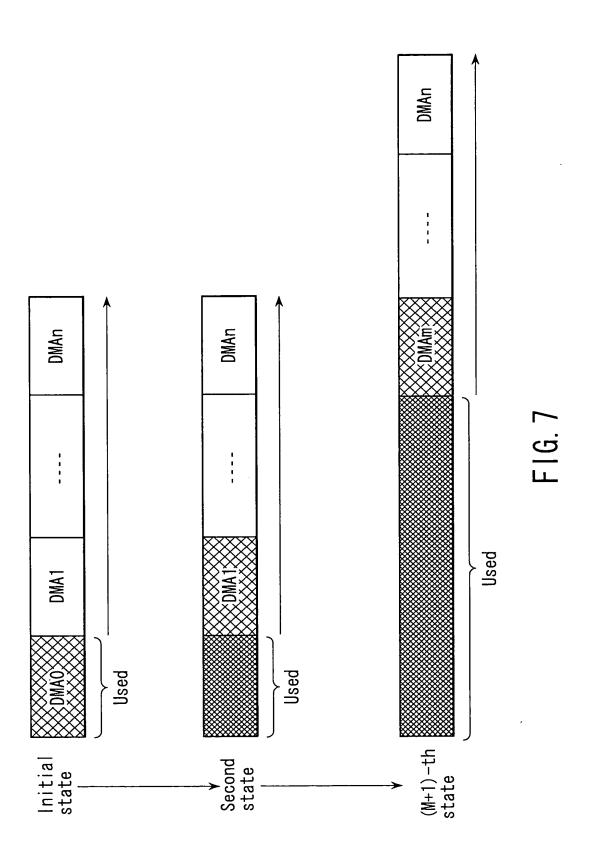
F1G. 5

SDL Entry

•	Replacement course		Ronlacement doctingtion
RSV (1B)	_	RSV (1R)	מבלו מכנוום וומרוחוו
)	(address (3B)	3-1	address (3B)

F1G. 6







DMA use state 1

	DDS/PDL update counter	SDL update counter	DMA counter
DMA 0	Normal use	Normal use	0 ~ Nov−1
DMA 1	Continuing use of above value	Continuing use of above value	0~Nov−1
	Continuing use of above value	Continuing use of above value	0~Nov−1
<u>.</u> -			
DMA m	Continuing use of above value	Continuing use of above value	0~Nov−1
	Continuing use of above value	Continuing use of above value	0~Nov−1
DMA n	Continuing use of above value	Continuing use of above value	0 ~Nov−1

Nov:Allowable overwrite count

F1G. 8



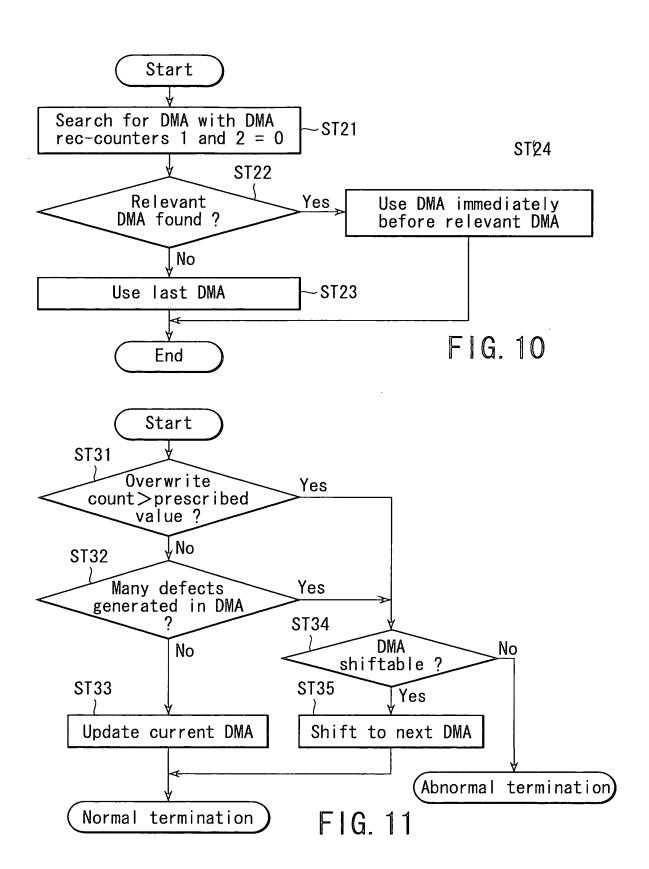
DMA use state 2

	DDS/PDL update counter	SDL update counter	DMA counter
DMA 0	Normal use	Normal use	0~Nov−1
DMA 1	Normal use after reset	Normal use after reset	0~Nov−1
	Normal use after reset	Normal use after reset	0∼Nov−1
DMA m	Normal use after reset	Normal use after reset	0~Nov-1
	Normal use after reset	Normal use after reset	0~Nov−1
DMA n	Normal use after reset	Normal use after reset	0∼Nov-1

Nov:Allowable overwrite count

F16.9



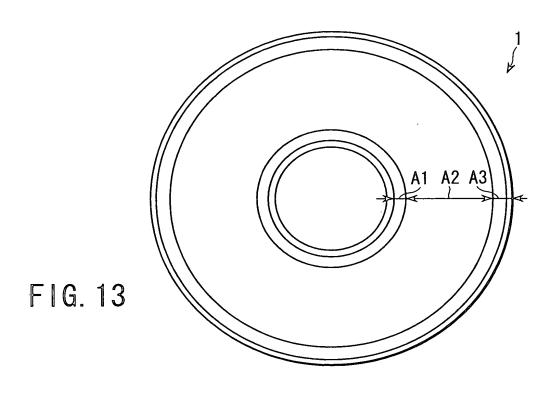


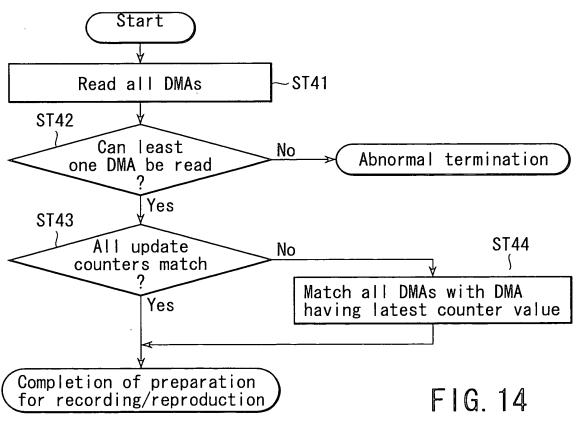
NOV 0 7 2000 25

] For innermost]∫ periphery	For]∫ butermost]∫ periphery]\ For innermost]∫ periphery	For]∫ outermost]∫ periphery	
DMAn	DMAn	DMAn	DMAn	ui þe	DMAn	DMAn	DMAn	DMAn	dnences
	1 1 1		1	Many defects are generated in DMA sequence 3	1 1 1	1 1 1	1 1 1		Shift to Next DMA in all sequences
DMA1	DMA1	DMA1	DMA1	Many de DMA sequ	DMA1	₩MA1	₩	₩ DMA1	Shift to N
1 SOUND	2 DMAO	3 DMAO	4 DMAO	In use	-	2	3	4	In use
DMA sequence	DMA sequence	DMA sequence	DMA sequence		DMA sequence	sednence	DMA sequence	DMA sequence	
DM		state DM	WO	,	DW	Second DMA	state DM	WQ_	,

F1G. 12









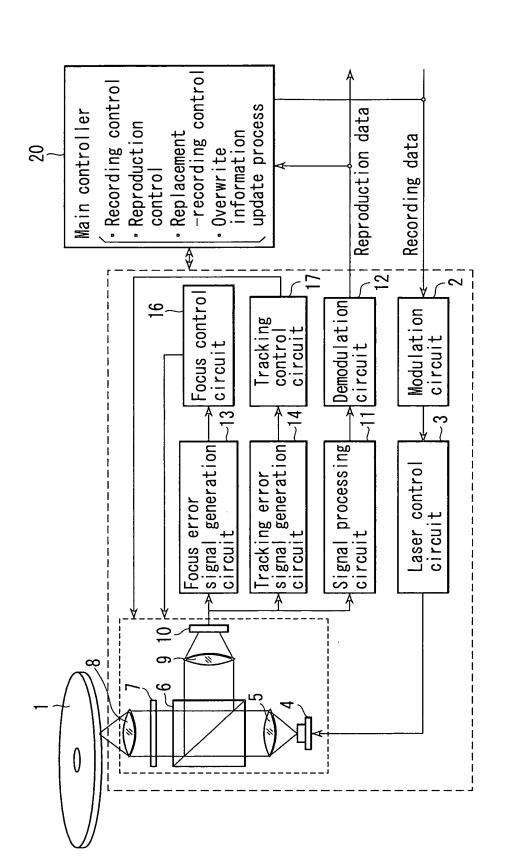


FIG. 15